



### CALL FOR PARTICIPATION -- HEART 2018

The 9th International Symposium on Highly Efficient Accelerators and Reconfigurable Technologies (HEART)

June 20-22, 2018  
University of Toronto  
Toronto, Canada

<http://heart2018.eecg.toronto.edu>

EARLY REGISTRATION DEADLINE: June 1, 2018

#### PROGRAM LAYOUT:

[http://heart2018.eecg.toronto.edu/pdf/HEART\\_2018\\_program.pdf](http://heart2018.eecg.toronto.edu/pdf/HEART_2018_program.pdf)

The Ninth International Symposium on Highly Efficient Accelerators and Reconfigurable Technologies (HEART) is a forum to present and discuss new research on accelerators and the use of reconfigurable technologies for high-performance and/or power-efficient computation.

### INVITED TALKS AND TECHNICAL PROGRAM

"Digitally Mapping the Universe",  
Prof. Keith Vanderlinde, Dunlap Institute

"GPUs as the Catalyst for Innovation in Artificial Intelligence", Niles Burbank, AMD

"Why FPGAs Are the Perfect Spatial Processor for Deep Learning Acceleration", Dr. Andrew Ling, Intel

"Near-Memory and In-Memory Inference",  
Dr. Martin Snelgrove, Untether AI

"Accelerating Memcached with Cloud-Deployed FPGAs", Dr. Jongsok Choi, LegUp Computing Inc.

The 2018 technical program includes original research papers on wide ranging aspects of compute acceleration with GPUs and FPGAs, novel reconfigurable hardware architectures, applications and design methodologies.

### VENUE

The conference will be held at the beautiful and historic University of Toronto Faculty Club, on the downtown Toronto campus of the University of Toronto. The Faculty Club is well known for its collection of Canadian Art.

<https://www.facultyclub.utoronto.ca/>

### SOCIAL PROGRAM

HEART 2018 registration includes access to the technical sessions, lunches, light breakfast, coffee breaks, and the conference banquet on the evening of June 20.

The HEART 2018 social event will be a banquet boat cruise on Lake Ontario, with stunning views of the Toronto skyline and islands.

### WORKSHOP

Date: June 22, 2018

Xilinx will present a hands-on workshop entitled, "FPGA-based Accelerated Cloud Computing with AWS EC2 F1 and SDAccel".

### CORPORATE SPONSORS

Huawei, Xilinx Inc., Intel Corp.

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